E UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Tetsuya HIRAOKA et al.

Group Art Unit: 2827

Serial No.: 09/973,002

Examiner: Lourdes C. Cruz

Filed: October 10, 2001

P.T.O. Confirmation No.: 4785

SEMICONDUCTOR DEVICE WITH STACK OF SEMICONDUCTOR CHIPS For.

PETITION FOR EXTENSION OF TIME

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Date: July 30

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated January 30, 2003 for the second and third months from May 30, 2003 to July 30, 2003.

Attached please find a check in the amount of \$820.00 (calculated as \$930.00 minus \$110.00 for the first month paid on May 30, 2003) to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP

07/31/2003 JBALIHAN 00000062 09973002

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820.00 OP

Mel R. Quintos Attorney for Applicants Reg. No. 31,898

MRQ/lrj Atty. Docket No. 011349 **Suite 1000** 1725 K Street, N.W. Washington, D.C. 20006 (202) 659-2930

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PATENT TRADEMARK OFFICE